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(71) Applicant (for all designated States except US): INTEL CORPORATION [US/US]; 2200 Mission College Boulevard, Santa Clara, CA 95052 (US).

(72) Inventors; and

(75) Inventors/Applicants (for US only): TSAI, Vicki [US/US]; 112 Hilary Avenue, Mountain View, CA 94040 (US). CHEN, Inching [CN/US]; 12869 NW Lorraine Drive, Portland, OR 97229 (US).

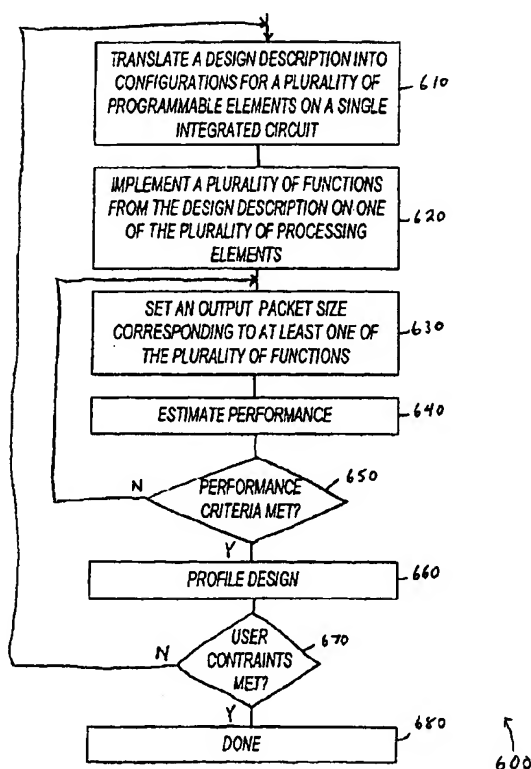
(74) Agents: LEMOINE, Dana, B. et al.; Lemoine Patent Services, PLLC, c/o Portfolio IP, P.O. Box 52050, Minneapolis, MN 55402 (US).

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[Continued on next page]

(54) Title: BLOCK-BASED PROCESSING IN A PACKET-BASED RECONFIGURABLE ARCHITECTURE



(57) Abstract: A reconfigurable circuit including a heterogeneous mix of processing elements is configured to use variable packet sizes.

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B. FIELDS SEARCHED

 Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	E. TSUI ET AL: "A New Distributed DSP Architecture Based on the Intel IXS for Wireless Client and Infrastructure (Hot Chips 14, Session 7: Digital Signal Processors, 20 August 2002)" HOT CHIPS 14 : CONFERENCE RECORD ; AUGUST 18 - 20, 2002, MEMORIAL AUDITORIUM, STANFORD UNIVERSITY, PALO ALTO, CALIFORNIA / SPONSORED BY THE IEEE COMPUTER SOCIETY TECHNICAL COMMITTEE ON MICROPROCESSORS AND MICROCOMPUTERS, 2002, pages 1-17, XP009053119 Palo Alto, Calif. slides 2,14-18, 24-27 ----- -/--	1-30

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☒ Patent family members are listed in annex.

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Name and mailing address of the ISA

 European Patent Office, P.B. 5818 Patentlaan 2
 NL - 2280 HV Rijswijk
 Tel. (+31-70) 340-2040. Tx. 31 651 epo nl
 Fax: (+31-70) 340-3016

Authorized officer

Schneider, M

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	COMPTON K HAUCK: "Reconfigurable computing: A survey of systems and software" ACM COMPUTING SURVEYS, ACM, NEW YORK, US, US, vol. 34, no. 2, June 2002 (2002-06), page 171AND210, XP002957662 ISSN: 0360-0300 sections 3.3 and 3.7, introduction of section 4, section 5.1.2 -----	1-30
Y	LETTIERI P ET AL: "Adaptive frame length control for improving wireless link throughput, range, and energy efficiency" INFOCOM '98. SEVENTEENTH ANNUAL JOINT CONFERENCE OF THE IEEE COMPUTER AND COMMUNICATIONS SOCIETIES. PROCEEDINGS. IEEE SAN FRANCISCO, CA, USA 29 MARCH-2 APRIL 1998, NEW YORK, NY, USA, IEEE, US, vol. 2, 29 March 1998 (1998-03-29), pages 564-571, XP010270390 ISBN: 0-7803-4383-2 abstract; page 566, right column, lines 7-36; page 570, right column, fourth paragraph -----	1-30
Y	GILFEATHER P ET AL: "Fragmentation and high performance IP" PARALLEL AND DISTRIBUTED PROCESSING SYMPOSIUM., PROCEEDINGS 15TH INTERNATIONAL SAN FRANCISCO, CA, USA 23-27 APRIL 2001, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 23 April 2001 (2001-04-23), pages 1690-1697, XP010544583 ISBN: 0-7695-0990-8 sections 1.2.3 and 8.2 -----	1-30
Y	EP 1 315 335 A (AT&T CORP) 28 May 2003 (2003-05-28) abstract paragraphs '0011!, '0024!, '0027!, '0035!, '0038!, '0039!; claim 1; figures 8,20-37 ----- -/--	1-30

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	<p>CHANDRANMENON G P ET AL: "RECONSIDERING FRAGMENTATION AND REASSEMBLY" PROCEEDINGS OF THE 17TH ANNUAL ACM SYMPOSIUM ON PRINCIPLES OF DISTRIBUTED COMPUTING.PODC 1998. PUERTO VALLARTA, MEXICO, JUNE 28 - JULY 2, 1998, ACM SIGACT - SIGMOD SYMPOSIUM ON PRINCIPLES OF DISTRIBUTED COMPUTING, NEW YORK, NY : ACM, US, 28 June 1998 (1998-06-28), pages 21-29, XP002921718 ISBN: 0-89791-877-7 page 23, left column, third paragraph; table 1</p> <p>-----</p>	1-30

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